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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/675,855	09/29/2000	Gary Dan Dotson	00AB147 (81696/235)	9316
7590 06/25/2004		EXAMINER		
Rockwell Technologies, LLC			TORRES, JOSEPH D	
Attention: John J. Horn			ART UNIT	PAPER NUMBER
Patent Dept./704P Floor 8 T-29 1201 South Second Street			2133	TAI ER NOMBER
Milwaukee, WI				
			DATE MAILED: 06/25/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

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Advisory Action

Application No.	Applicant(s)	
09/675,855	DOTSON, GARY D	AN A
Examiner	Art Unit	
Joseph D. Torres	2133	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 17 May 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

Examination (RCE) in compliance with 37 CFR 1.114.
PERIOD FOR REPLY [check either a) or b)]
 a)
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).
1. A Notice of Appeal was filed on Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2. The proposed amendment(s) will not be entered because:
(a) _ they raise new issues that would require further consideration and/or search (see NOTE below);
(b) they raise the issue of new matter (see Note below);
(c) \(\sum \) they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
(d) \square they present additional claims without canceling a corresponding number of finally rejected claims.
NOTE:
3. Applicant's reply has overcome the following rejection(s):
4. Newly proposed or amended claim(s) would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5. ☐ The a) ☐ affidavit, b) ☐ exhibit, or c) ☐ request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6. The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7. For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.
The status of the claim(s) is (or will be) as follows:
Claim(s) allowed: 11-23.
Claim(s) objected to:
Claim(s) rejected: <u>1 and 4-10</u> .
Claim(s) withdrawn from consideration:
8. The drawing correction filed on is a) approved or b) disapproved by the Examiner.
9. Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s)
10. Other:
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U.S. Patent and Trademark Office PTOL-303 (Rev. 11-03)

Advisory Action

Part of Paper No. 20040623

Continuation of 5. does NOT place the application in condition for allowance because: The Applicant contends, "Wolf does not teach or suggest an arithmetic circuit employed to provide error checking based on data received from the DMA controller".

The Examiner disagrees and asserts that one of ordinary skill in the art at the time the invention was made would have known that memory access is completely under the control of the DMA controller and that a DMA controller must provide a multitude of control commands during memory access (Note: error correction is a read access operation), hence error correction operations are based on control data provided from the DMA to read access circuitry and, in fact, column 8 of Wolf provides interactions under the control of the DMA using control data provided to error correction circuitry to perform the error correction algorithms taught in Figures 12 and 13 of Wolf.

The Applicant contends, "simply stating that data is received from DMA is of no consequence when the data received is unrelated to the value of J0 and how the code generator polynomial is modified".

The Examiner asserts that nowhere in claim 1 does the Applicant claim any relationship between "how the code generator polynomial is modified" and "data received from the DMA controller" other than that the "arithmetic circuit employed to provide error checking based on data received from the DMA controller", which does not necessarily imply that the code generator polynomial is modified based on data received from the DMA controller. If the Applicant wishes to claim such a limitation, the Applicant must explicitly recite the limitation on the claims.

The Applicant contends, "Furthermore, thee is no teaching, suggestion or motivation in Wolf or McSpadden to combine the subject reference to teach a programmable linear feedback shift register (LFSR)".

The Examiner would like to point out that Wolf teaches an arithmetic unit that requires use of a means, capable of being modified according to a specific generator polynomial, for generating code and syndromes based on the specific generator polynomial in use. One of ordinary skill in the art at the time the invention was made would have recognized the McSpadden provides the means for implementing the adaptive arithmetic unit taught in the Wolf patent.

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